

Title (en)

TWO CLOCK DOMAIN PULSE TO PULSE SYNCHRONIZER

Title (de)

SCHALTUNG ZUR SYNCHRONISATION VON IMPULSEN, DIE IM JEWEILS ANDEREN GELTUNGSBEREICH ZWEIER VERSCHIEDENER TAKTSIGNALE ERZEUGT WERDEN

Title (fr)

SYNCHRONISEUR D'IMPULSION A IMPULSION A DEUX DOMAINES D'HORLOGE

Publication

EP 1188108 B1 20031126 (EN)

Application

EP 00925576 A 20000417

Priority

IT 0000153 W 20000417

Abstract (en)

[origin: WO0179987A1] A synchronization circuit (P2P) for interfacing a first digital circuit functioning with a first clock (CLK1) and a second digital circuit functioning with a second clock (CLK2) that may be different from the first clock (CLK1) in terms of frequency and/or phase is constituted by a transmitting section functioning with the first clock (CLK1), a receiving section functioning with the second clock (CLK2) and a feedback section functioning with the first clock (CLK1). A bidirectional synchronizer transfers commands with a strobe signal (STR) from a microprocessor interface (MPRI) functioning with the first clock (CLK1) to an application circuit (APL) functioning with the second clock (CLK2) and alarm signals from the application circuit (APL) to the microprocessor interface (MPRI).

IPC 1-7

G06F 5/06

IPC 8 full level

G06F 5/06 (2006.01); **H04L 7/02** (2006.01)

CPC (source: EP)

G06F 5/06 (2013.01); **G06F 2205/102** (2013.01); **H04L 7/02** (2013.01)

Designated contracting state (EPC)

DE FR GB IT NL SE

DOCDB simple family (publication)

WO 0179987 A1 20011025; DE 60006812 D1 20040108; EP 1188108 A1 20020320; EP 1188108 B1 20031126

DOCDB simple family (application)

IT 0000153 W 20000417; DE 60006812 T 20000417; EP 00925576 A 20000417