

Title (en)

SMART CARD ARCHITECTURE INCORPORATING PERIPHERALS

Title (de)

ARCHITEKTUR FÜR CHIPKARTE MIT INTEGRIERTEN PERIPHERIEGERÄTEN

Title (fr)

ARCHITECTURE DE CARTE A PUCE INTEGRANT DES PERIPHERIQUES

Publication

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Application

**EP 00954696 A 20000713**

Priority

- FR 0002046 W 20000713
- FR 9909818 A 19990728

Abstract (en)

[origin: FR2797074A1] The invention concerns a portable electronic device, such as a smart card incorporating at least a peripheral and comprising at least a security component and an external communication interface. The invention is characterised in that it further comprises a peripheral manager including at least a smart card reading function to communicate with the security component.

IPC 1-7

**G06K 19/07**; **G06K 19/073**; **G06K 7/00**; **G07F 7/10**

IPC 8 full level

**G07F 7/10** (2006.01)

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