

Title (en)

METHOD AND TEST DEVICE FOR DETERMINING AND CORRECTING AN OFFSET IN DIRECT VOLTAGE ON A MODULATOR

Title (de)

VERFAHREN UND TESTGERÄT ZUR ERMITTLUNG UND KORREKTUR EINES GLEICHSPANNUNGSVERSATZES AN EINEM MODULATOR

Title (fr)

PROCEDE ET APPAREIL DE TEST POUR LA DETERMINATION ET LA CORRECTION D'UN DECALAGE DE TENSION CONTINUE DANS UN MODULATEUR

Publication

**EP 1228617 A1 20020807 (DE)**

Application

**EP 00956106 A 20000726**

Priority

- DE 0002464 W 20000726
- DE 19953873 A 19991109

Abstract (en)

[origin: WO0135592A1] The invention relates to a method for determining an offset in direct voltage and/or at least one correction value therefor on a modulator which, by modulating a digital or analog input signal on a transmission carrier, generates an output signal in which the information is located at least in the instantaneous frequency thereof or in its phase progression. The invention is characterized in that the offset in direct voltage and/or the correction value are determined in a directly mathematical manner from the progression of the phase error of the output signal with regard to an ideal signal.

IPC 1-7

**H04L 27/36**; H04L 1/24; H04L 27/20

IPC 8 full level

**H03C 3/40** (2006.01); **H04L 1/24** (2006.01); **H04L 27/20** (2006.01); **H04L 27/36** (2006.01)

CPC (source: EP)

**H03C 3/40** (2013.01); **H04L 1/24** (2013.01); **H04L 27/20** (2013.01); **H04L 27/364** (2013.01)

Designated contracting state (EPC)

DE FR GB

DOCDB simple family (publication)

**WO 0135592 A1 20010517**; CN 1390413 A 20030108; EP 1228617 A1 20020807; HU P0204076 A2 20030428

DOCDB simple family (application)

**DE 0002464 W 20000726**; CN 00815501 A 20000726; EP 00956106 A 20000726; HU P0204076 A 20000726