

Title (en)

AMPLIFIER WITH IMPROVED, HIGH PRECISION, HIGH SPEED AND LOW POWER CONSUMPTION ARCHITECTURE

Title (de)

VERSTÄRKER MIT HOCHLINEARER, SCHNELLEN ARCHITEKTUR UND GERINGEM LEISTUNGSVERBRAUCH

Title (fr)

AMPLIFICATEUR A ARCHITECTURE DE HAUTE PRECISION, RAPIDE ET A FAIBLE CONSOMMATION D'ENERGIE

Publication

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Application

EP 00931505 A 20000615

Priority

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- IT MI991371 A 19990618

Abstract (en)

[origin: WO0079680A1] In an amplifier with improved, high precision, high speed and low power consumption architecture, comprising an input stage (1), an intermediate stage (2) and an output stage (3), the intermediate stage (2) is provided by combining two complementary class AB operating halves (2a, 2b) which are driven by a complementary input stage, which is, in turn, class AB operating, one of said halves having a low limited current gain (GU), conveniently but not necessarily controlled, and the other one having a high or very high current gain (GD) which can, but not necessarily, be controlled. Suitably the input class AB operating input stage (1) of this amplifier provides two pairs or one pair of antiphase output currents (IR and IL), apt to be used to feed both halves (2a, 2b) of the intermediate stage.

IPC 1-7

H03F 3/30

IPC 8 full level

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