

Title (en)

POWER SAVING FOR MAC ETHERNET CONTROL LOGIC

Title (de)

ENERGIESPAREN FÜR EINE MAC-ETHERNET-STEUERLOGIK

Title (fr)

GESTION D'ENERGIE POUR UNE LOGIQUE DE CONTROLE ETHERNET MAC

Publication

**EP 1243098 A2 20020925 (EN)**

Application

**EP 00986526 A 20001218**

Priority

- US 0034344 W 20001218
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Abstract (en)

[origin: WO0147188A2] A media access controller (100) having a power-saving feature. The controller (100) comprises a receive logic circuit for receiving incoming data from a physical interface device (104) and processing the incoming data for transmission to a frame processor (102), and a transmit logic circuit for receiving outgoing data of the frame processor (102) and processing the outgoing data for transmission to the physical interface device (104). A power management control logic (114) operatively connects to each of the receive logic circuit and the transmit logic circuit to control the receive logic circuit and the transmit logic circuit in a first mode or a second mode. The power management control logic (114) controls the media access controller (100) in the first mode to conserve power by stopping operation of substantial portions of both the receive and transmit logic circuits, and in the second mode, which is a full power mode, by running both the receive and transmit logic circuits.

IPC 1-7

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IPC 8 full level

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