

Title (en)  
Active matrix display device

Title (de)  
Anzeigegerät mit aktiver Matrix

Title (fr)  
Dispositif d'affichage à matrice active

Publication  
**EP 1251483 A3 20030402 (EN)**

Application  
**EP 02008513 A 20020415**

Priority  
• JP 2001115371 A 20010413  
• JP 2001115372 A 20010413  
• JP 2001115374 A 20010413  
• JP 2001115375 A 20010413

Abstract (en)  
[origin: EP1251483A2] A display device capable of analog and digital image display has a retaining circuit (110) holding an image signal disposed for each of the pixel elements. In the memory operation mode, an output from an oscillation unit (300) formed inside the display panel is supplied to the pixel element electrodes of the display. The on-resistance of an output transistor (303) for the pixel electrode is higher than the on-resistance of an thin film transistors of the inverters (307, 308) in the oscillation unit. In the memory operation mode, gate and drain lines are set at predetermined voltage and an output of a voltage booster circuit (200) formed in the display panel is used as a reference voltage of the retaining circuit (110) and is also used for switching a selection circuit (41, 42) selecting image display circuits. <IMAGE>

IPC 1-7  
**G09G 3/36; G09G 3/32**

IPC 8 full level  
**G02F 1/133** (2006.01); **G09G 3/20** (2006.01); **G09G 3/32** (2006.01); **G09G 3/36** (2006.01)

CPC (source: EP KR US)  
**G02F 1/133** (2013.01 - KR); **G09G 3/2011** (2013.01 - EP US); **G09G 3/3291** (2013.01 - EP US); **G09G 3/3648** (2013.01 - EP US);  
**G09G 3/3659** (2013.01 - EP US); **G09G 3/3233** (2013.01 - EP US); **G09G 3/3258** (2013.01 - EP US); **G09G 2300/0809** (2013.01 - EP US);  
**G09G 2300/0842** (2013.01 - EP US); **G09G 2300/0857** (2013.01 - EP US); **G09G 2310/0251** (2013.01 - EP US);  
**G09G 2330/02** (2013.01 - EP US); **G09G 2330/021** (2013.01 - EP US)

Citation (search report)  
• [PX] EP 1136978 A2 20010926 - TOSHIBA KK [JP]  
• [PA] EP 1180762 A2 20020220 - SHARP KK [JP]  
• [A] US 5945972 A 19990831 - OKUMURA HARUHIKO [JP], et al

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EP1901278A3; US7816683B2

Designated contracting state (EPC)  
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE TR

DOCDB simple family (publication)  
**EP 1251483 A2 20021023; EP 1251483 A3 20030402**; CN 1260700 C 20060621; CN 1381823 A 20021127; EP 1557816 A2 20050727;  
KR 100497455 B1 20050701; KR 20020079599 A 20021019; TW I236558 B 20050721; US 2002171607 A1 20021121; US 6950080 B2 20050927

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**EP 02008513 A 20020415**; CN 02105729 A 20020415; EP 05009038 A 20020415; KR 20020019921 A 20020412; TW 91106569 A 20020402;  
US 12214602 A 20020415