

Title (en)

Method for implementing error diffusion on plasma display panel

Title (de)

Verfahren zur Durchführung von Fehlerdiffusion für eine Plasmaanzeigetafel

Title (fr)

Méthode de mise en oeuvre de traitement d'images par diffusion des erreurs pour un panneau d'affichage à plasma

Publication

EP 1262941 A1 20021204 (EN)

Application

EP 01113011 A 20010528

Priority

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Abstract (en)

A method for implementing error diffusion on a plasma display panel (PDP) comprises the steps of performing an anti compensation process on a received video signal of the PDP; diffusing an error generated by a first one of a plurality of pixels to a plurality of adjacent pixels; absorbing errors generated by the plurality of adjacent pixels by the first pixel; and multiplying each of a plurality of numeric weightings and the error of each of the adjacent pixels to obtain an error function of the first pixel. This is effected in a cost effective while simple addition circuit for solving the problem of low level contouring in PDP due to insufficient gray scale of video signal in low gray scale. <IMAGE>

IPC 1-7

G09G 3/28

IPC 8 full level

G09G 3/20 (2006.01); **G09G 3/28** (2006.01)

CPC (source: EP)

G09G 3/2022 (2013.01); **G09G 3/2059** (2013.01); **G09G 3/2077** (2013.01); **G09G 3/28** (2013.01); **G09G 2320/0266** (2013.01); **G09G 2320/0271** (2013.01); **G09G 2320/0276** (2013.01)

Citation (search report)

- [X] EP 0893916 A2 19990127 - MATSUSHITA ELECTRIC IND CO LTD [JP]
- [X] EP 1022714 A2 20000726 - PIONEER CORP [JP]
- [A] EP 1020838 A1 20000719 - PIONEER CORP [JP]

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