

Title (en)

METHOD AND APPARATUS FOR A GATED OSCILLATOR IN DIGITAL CIRCUITS

Title (de)

VERFAHREN UND GERÄT FÜR EINEN GESCHALTETEN OSZILLATOR IN DIGITALEN SCHALTUNGEN

Title (fr)

PROCEDE ET APPAREIL POUR UN OSCILLATEUR A DECLENCHEMENT AUTOMATIQUE DANS DES CIRCUITS NUMERIQUES

Publication

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Application

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Priority

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Abstract (en)

[origin: WO0182481A1] A gated oscillator (100) is provided for digital circuits. The gated oscillator is achieved by the unconventional use of controlling the operating point of a Van der Pol oscillator. Oscillations are achieved by Van der Pol self-oscillation behavior. The result is a highly simplified gated oscillator circuit for many digital circuit applications.

IPC 1-7

H03K 3/313; **H03K 3/315**

IPC 8 full level

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CPC (source: EP)

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Citation (search report)

See references of WO 0182481A1

Citation (examination)

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