

Title (en)

Circuit board and SMD-antenna thereof

Title (de)

Schaltungsplatine und SMD-Antenne hierfür

Title (fr)

Platine de circuit et antenne pour montage en surface (SMD) correspondante

Publication

EP 1289053 A3 20040204 (DE)

Application

EP 02102278 A 20020903

Priority

DE 10143168 A 20010904

Abstract (en)

[origin: EP1289053A2] An SMD (Surface Mount Device) antenna has a ceramic substrate (1) with resonant radiating trips (20) bonded to the ground plane (41) and capacitive coupling (16, 17) to input line (42) and ground plane.

IPC 1-7

H01Q 1/38; **H01Q 9/42**; **H01Q 21/30**; **H01Q 5/00**; **H01Q 1/24**

IPC 8 full level

H05K 1/18 (2006.01); **H01Q 1/24** (2006.01); **H01Q 1/38** (2006.01); **H01Q 5/357** (2015.01); **H01Q 5/378** (2015.01); **H01Q 5/40** (2015.01); **H01Q 9/42** (2006.01); **H01Q 21/30** (2006.01); **H05K 1/02** (2006.01)

CPC (source: EP KR US)

H01Q 1/243 (2013.01 - EP US); **H01Q 1/38** (2013.01 - EP KR US); **H01Q 5/357** (2015.01 - EP US); **H01Q 5/378** (2015.01 - EP US); **H01Q 5/40** (2015.01 - EP US); **H01Q 9/42** (2013.01 - EP US); **H01Q 21/30** (2013.01 - EP US)

Citation (search report)

- [X] US 6177908 B1 20010123 - KAWAHATA KAZUNARI [JP], et al
- [X] EP 1122812 A2 20010808 - MURATA MANUFACTURING CO [JP]
- [X] PATENT ABSTRACTS OF JAPAN vol. 2000, no. 25 12 April 2001 (2001-04-12)

Cited by

US7830330B2; US7463196B2

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LI LU MC NL PT SE SK TR

DOCDB simple family (publication)

EP 1289053 A2 20030305; **EP 1289053 A3 20040204**; CN 1417888 A 20030514; DE 10143168 A1 20030320; JP 2003163528 A 20030606; KR 20030020841 A 20030310; US 2003043081 A1 20030306; US 2004113846 A1 20040617; US 6683576 B2 20040127; US 6958731 B2 20051025

DOCDB simple family (application)

EP 02102278 A 20020903; CN 02151470 A 20020902; DE 10143168 A 20010904; JP 2002259006 A 20020904; KR 20020052689 A 20020903; US 23479902 A 20020904; US 72374503 A 20031126