

Title (en)

ENHANCEMENTS TO OBJECT-ORIENTED ELECTRONIC CIRCUIT DESIGN MODELING AND SIMULATION ENVIRONMENT

Title (de)

VERBESSERUNG DER MODELLIERUNG EINES OBJEKTORIENTIERTEN ELEKTRONISCHEN SCHALTUNGSENTWURFS UND SIMULATIONSUMGEBUNG

Title (fr)

AMELIORATIONS APORTEES A UN ENVIRONNEMENT DE MODELISATION ET DE SIMULATION DE CONCEPTIONS D'UN CIRCUIT ELECTRONIQUE ORIENTE OBJET

Publication

EP 1290588 A2 20030312 (EN)

Application

EP 01920629 A 20010322

Priority

- US 0109106 W 20010322
- US 57750900 A 20000524

Abstract (en)

[origin: WO0190961A2] An apparatus, program product and method enhance the modeling and stimulation of electronic circuit designs on computers, particularly in the area of object-oriented modeling and simulation. Multiplexed communication ports (130) may be supported that support the propagation of values for multiple signals in response to single events directed to such ports (130), thus logically combining multiple signal paths together into a single logical path. Dynamic instruction decoder generation (160) may also be supported, such that an instruction definition that defines an instruction capable of being executed by a processing system model may be utilized in the configuration of an instruction decoder circuit component (150') for the processing system model to stimulate execution of the instruction.

IPC 1-7

G06F 17/50

IPC 8 full level

G06F 17/50 (2006.01)

CPC (source: EP)

G06F 30/33 (2020.01)

Citation (search report)

See references of WO 0190961A2

Designated contracting state (EPC)

DE FR GB

DOCDB simple family (publication)

WO 0190961 A2 20011129; WO 0190961 A3 20021205; EP 1290588 A2 20030312; JP 2003534603 A 20031118

DOCDB simple family (application)

US 0109106 W 20010322; EP 01920629 A 20010322; JP 2001587278 A 20010322