

Title (en)
SIMPLE CHIP IDENTIFICATION

Title (de)
EINFACHE CHIPIDENTIFIZIERUNG

Title (fr)
IDENTIFICATION SIMPLE D'UNE PUCE

Publication
EP 1299883 A2 20030409 (EN)

Application
EP 01962828 A 20010705

Priority

- EP 0107674 W 20010705
- US 61458300 A 20000712

Abstract (en)
[origin: WO0209114A2] A technique for identifying circuits within an electrical device is disclosed. In one embodiment, additional current sink paths are associated with one or more circuits within the electrical device and measurements of additional current drawn by the current sink paths can be used to identify the associated circuit. In an additional embodiment, a special mode of operation is implemented in the electrical device where the circuit to be identified outputs identity signals in response to control signals from a controller. In a further embodiment, a shift register is activated in the special mode of operation to serially output an identity word in response to the control signals from the controller.

IPC 1-7
G11C 5/00

IPC 8 full level
G11C 5/00 (2006.01)

CPC (source: EP KR US)
G01R 31/00 (2013.01 - KR); **H01L 23/544** (2013.01 - EP US); **H01L 2223/54433** (2013.01 - EP US); **H01L 2223/5444** (2013.01 - EP US);
H01L 2924/0002 (2013.01 - EP US)

Citation (search report)
See references of WO 0209114A2

Designated contracting state (EPC)
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU MC NL PT SE TR

DOCDB simple family (publication)
WO 0209114 A2 20020131; WO 0209114 A3 20020718; AT E331287 T1 20060715; AU 8392301 A 20020205; DE 60120978 D1 20060803;
DE 60120978 T2 20061109; EP 1299883 A2 20030409; EP 1299883 B1 20060621; KR 20030015887 A 20030225; MY 124516 A 20060630;
US 6483335 B1 20021119

DOCDB simple family (application)
EP 0107674 W 20010705; AT 01962828 T 20010705; AU 8392301 A 20010705; DE 60120978 T 20010705; EP 01962828 A 20010705;
KR 20037000530 A 20030113; MY PI20012945 A 20010621; US 61458300 A 20000712