

Title (en)

Integrated circuit with closely coupled high-voltage output and offline transistor pair.

Title (de)

Integrierter Schaltkreis mit eng gekoppeltem Hochspannungsausgang und "Offline" Transistorpaar

Title (fr)

Circuit intégré comportant une paire de transistors à haute tension de sortie et "offline" étroitement couplée

Publication

EP 1309006 B1 20090617 (EN)

Application

EP 02255727 A 20020816

Priority

US 5353801 A 20011102

Abstract (en)

[origin: EP1309006A2] An integrated circuit fabricated in a single silicon substrate includes a high-voltage output transistor having source and drain regions separated by a channel region, and a gate disposed over the channel region. Also included is an offline transistor having source and drain regions separated by a channel region and a gate disposed over the channel region of the offline transistor. A drain electrode is commonly coupled to the drain region of the high-voltage output transistor and to the drain region of the offline transistor.

IPC 8 full level

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CPC (source: EP US)

H01L 27/0705 (2013.01 - EP US); **H01L 2224/05553** (2013.01 - EP US); **H01L 2224/05554** (2013.01 - EP US);
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C-Set (source: EP US)

H01L 2924/13091 + **H01L 2924/00**

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DOCDB simple family (publication)

EP 1309006 A2 20030507; **EP 1309006 A3 20070822**; **EP 1309006 B1 20090617**; AT E434267 T1 20090715; DE 60232635 D1 20090730;
EP 2093799 A2 20090826; EP 2093799 A3 20100310; JP 2003264241 A 20030919; JP 4125567 B2 20080730; US 2003112052 A1 20030619;
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EP 02255727 A 20020816; AT 02255727 T 20020816; DE 60232635 T 20020816; EP 09162684 A 20020816; JP 2002258267 A 20020731;
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