

Title (en)

Analog phase locked oscillator

Title (de)

Analoger phasenverriegelter Oszillator

Title (fr)

Oscillateur analogique verrouillé en phase

Publication

**EP 1313224 A3 20040128 (EN)**

Application

**EP 02257923 A 20021115**

Priority

JP 2001351560 A 20011116

Abstract (en)

[origin: EP1313224A2] There is provided a voltage-controlled oscillator (5) whose oscillation frequency is controlled by a control voltage, a reference oscillator (1), and a sampling phase detector (3) for receiving an oscillation signal of the voltage-controlled oscillator and a reference oscillation signal of the reference oscillator and for inputting, to the voltage-controlled oscillator, as the control voltage, an error voltage that is obtained on the basis of a phase difference between the oscillation signal and a harmonic of the reference oscillation signal. An irreversible circuit element (6) is provided between the voltage-controlled oscillator and the sampling phase detector. The oscillation signal that is output from the voltage-controlled oscillator is input to the sampling phase detector via the irreversible circuit element. <IMAGE>

IPC 1-7

**H03L 7/091**; **H03L 7/20**

IPC 8 full level

**H03L 7/091** (2006.01); **H03L 7/20** (2006.01)

CPC (source: EP US)

**H03L 7/091** (2013.01 - EP US); **H03L 7/20** (2013.01 - EP US)

Citation (search report)

- [XY] US 3277390 A 19661004 - MCLIN FRANK D
- [Y] PATENT ABSTRACTS OF JAPAN vol. 009, no. 274 (E - 354) 31 October 1985 (1985-10-31)
- [X] PATENT ABSTRACTS OF JAPAN vol. 013, no. 254 (E - 772) 13 June 1989 (1989-06-13)
- [Y] PATENT ABSTRACTS OF JAPAN vol. 014, no. 383 (E - 0966) 17 August 1990 (1990-08-17)

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