

Title (en)

A HIGH-EFFICIENCY DRIVER CIRCUIT FOR CAPACITIVE LOADS

Title (de)

TREIBERSCHALTUNG MIT HOHEM WIRKUNGSGRAD FÜR KAPAZITIVE LASTEN

Title (fr)

CIRCUIT D'ATTAQUE A HAUT RENDEMENT POUR CHARGES CAPACITIVES

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Application

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Abstract (en)

[origin: WO0225816A1] The invention relates to a high-efficiency driver circuit for capacitive loads, which circuit comprises a drive portion (12) connected to at least one end (X) of a capacitive electric load (11) being applied a voltage waveform. Advantageously, this circuit further comprises a switching circuit portion (13) having its output connected to the above one end (X) of the capacitive load (11) in order to supply a fraction of the overall current demanded by the load (11). Briefly, the invention is a circuit arrangement aimed at providing a highly efficient drive for the capacitive load, using a combined linear/switching setup and without debasing the quality of the waveform generated across the capacitive load.

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