

Title (en)

CIRCUIT FOR DETECTING AND FORMATTING DATA FRAMES

Title (de)

SCHALTUNG ZUR DETEKTION UND UMSETZUNG EINER DATENSPUR

Title (fr)

CIRCUIT DE DETECTION ET DE MISE EN FORME DE TRAMES DE DONNEES

Publication

**EP 1330781 A1 20030730 (FR)**

Application

**EP 00971483 A 20001023**

Priority

FR 0002946 W 20001023

Abstract (en)

[origin: WO0235466A1] The invention concerns non-contact smart cards and, more particularly in such cards, a circuit for detecting data frames and providing them with a parallel format for their processing. The invention is characterised in that it consists in using the information contained in the first octets to determining the number of octets of the frame being currently received, thereby enabling to identify them as they are received and route them into registers (80). This is provided by a state machine (60) whereof the shift from one state to the other is switched by the circuitry (62, 76) output signals.

IPC 1-7

**G06K 19/07**

IPC 8 full level

**H04L 7/08** (2006.01); **G06K 19/07** (2006.01)

CPC (source: EP US)

**G06K 19/07** (2013.01 - EP US); **G06K 19/0723** (2013.01 - EP US)

Citation (search report)

See references of WO 0235466A1

Designated contracting state (EPC)

DE FR GB IT

DOCDB simple family (publication)

**WO 0235466 A1 20020502**; EP 1330781 A1 20030730; JP 2004512754 A 20040422; US 7003407 B1 20060221

DOCDB simple family (application)

**FR 0002946 W 20001023**; EP 00971483 A 20001023; JP 2002538376 A 20001023; US 39981703 A 20030423