

Title (en)

Circuit for injecting test signals in an amplifier circuit

Title (de)

Schaltungsanordnung zur Einspeisung von Testsignalen in eine Verstärkungsschaltung

Title (fr)

Circuit d'alimentation des signaux de test dans un circuit d'amplification

Publication

**EP 1331491 A3 20041201 (DE)**

Application

**EP 02023765 A 20021024**

Priority

DE 10203562 A 20020129

Abstract (en)

[origin: DE10203562C1] A low resistance signal source (20) generates test signals. Two signal lines (21,22) are connected at high resistance to one of the two inputs of respective charge amplifiers (13,14), the inputs being connected to the hydrophone (10). One signal line carries the test signals, while the other carries test signals with a 180 degree phase shift. An electronic switch (29,30) with an attenuator (22,34) on its output is arranged in each signal line.

IPC 1-7

**G01S 7/526; G01S 15/04; G01S 15/06**

IPC 8 full level

**G01S 7/52 (2006.01); G01S 7/526 (2006.01)**

CPC (source: EP)

**G01S 7/52004 (2013.01); G01S 7/526 (2013.01)**

Citation (search report)

- [X] US 6046632 A 20000404 - STRAW TIMOTHY B [US]
- [Y] US 5477504 A 19951219 - HAGERTY JAMES D [US]
- [Y] US 4689578 A 19870825 - SPYCHALSKI STEPHEN E [US]
- [Y] US 4855685 A 19890808 - HOCHSCHILD JAMES R [US]
- [A] US 4972381 A 19901120 - MITCHELL BRUCE C [MG], et al

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LI LU MC NL PT SE SK TR

DOCDB simple family (publication)

**DE 10203562 C1 20030618; AU 2003200196 A1 20030814; AU 2003200196 B2 20070823; DE 50211286 D1 20080110;**  
**EP 1331491 A2 20030730; EP 1331491 A3 20041201; EP 1331491 B1 20071128**

DOCDB simple family (application)

**DE 10203562 A 20020129; AU 2003200196 A 20030122; DE 50211286 T 20021024; EP 02023765 A 20021024**