

Title (en)

Differential flash memory programming technique

Title (de)

Programmierungstechnik für differentiellen Flash-Speicher

Title (fr)

Technique de programmation pour une mémoire flash différentielle

Publication

**EP 1331643 B1 20091216 (EN)**

Application

**EP 02002166 A 20020129**

Priority

EP 02002166 A 20020129

Abstract (en)

[origin: EP1331643A1] The invention relates flash memory programming techniques. An object of the invention is to provide a flash memory programming technique avoiding problems of the known state of the art and in particular, saving a significant amount of time during the development and/or production phases of any equipment containing flash memory devices and also saving time during an updating or upgrading procedure of such an equipment already being in use. Accordingly, the invention proposes for programming a flash memory device to program only differences in information between data already stored in the flash memory device and new data to be stored.

IPC 8 full level

**G06F 9/445** (2006.01); **G06F 12/02** (2006.01); **G11C 16/10** (2006.01); **G11C 16/34** (2006.01)

CPC (source: EP US)

**G06F 8/654** (2018.01 - EP US); **G06F 8/658** (2018.01 - EP US); **G06F 12/0246** (2013.01 - EP US); **G11C 16/102** (2013.01 - EP US); **G11C 16/105** (2013.01 - EP US); **G06F 2212/7202** (2013.01 - EP US); **G11C 16/349** (2013.01 - EP US)

Cited by

CN111142916A; EP1837755A1; EP1755034A1; US8250560B2; WO2007020082A1

Designated contracting state (EPC)

GB

DOCDB simple family (publication)

**EP 1331643 A1 20030730**; **EP 1331643 B1 20091216**; EP 2148334 A1 20100127; EP 2148334 B1 20111109; US 2003142556 A1 20030731; US 2008068894 A1 20080320; US 2009296485 A1 20091203; US 7313030 B2 20071225; US 7593269 B2 20090922; US 7920425 B2 20110405

DOCDB simple family (application)

**EP 02002166 A 20020129**; EP 09014173 A 20020129; US 34905503 A 20030122; US 54112209 A 20090813; US 94350207 A 20071120