Title (en)
COMMUNICATION SYSTEM FOR EXCHANGING DATA USING AN ADDITIONAL PROCESSOR

Title (de)
KOMMUNIKATIONSSYSTEM ZUM AUSTAUSCH VON DATEN UNTER VERWENDUNG EINES ZUSÄTZLICHEN PROZESSORS

Title (fr)
SYSTEME DE COMMUNICATION POUR ECHANGER DES DONNEES AU MOYEN D'UN PROCESSEUR SUPPLEMENTAIRE

Publication
EP 1334432 A1 20030813 (DE)

Application
EP 01993881 A 20011025

Priority
• DE 0104081 W 20011025
• DE 10056198 A 20001113

Abstract (en)
[origin: DE10056198A1] A communications system has one or more serial interfaces (IF1-IF3) that connect to a common bus line as well as a first processor (1) that connects to the common bus line. Data exchange is regulated by a second processor (2) that connects to the common bus line and fits on one and the same chip (10) together with the first processor.
[origin: DE10056198A1] A communications system has one or more serial interfaces (IF1-IF3) that connect to a common bus line as well as a first processor (1) that connects to the common bus line. Data exchange is regulated by a second processor (2) that connects to the common bus line and fits on one and the same chip (10) together with the first processor.

IPC 1-7
G06F 13/12

IPC 8 full level
G06F 9/46 (2006.01); **G06F 13/12** (2006.01)

CPC (source: EP US)
G06F 13/124 (2013.01 - EP US)

Citation (search report)
See references of WO 0239292A1

Designated contracting state (EPC)
DE FR GB IT

DOCDB simple family (publication)
DE 10056198 A1 20020214; CN 1474970 A 20040211; EP 1334432 A1 20030813; JP 2004513457 A 20040430; US 2003233506 A1 20031218; WO 0239292 A1 20020516

DOCDB simple family (application)
DE 10056198 A 20001113; CN 01818691 A 20011025; DE 0104081 W 20011025; EP 01993881 A 20011025; JP 2002541547 A 20011025; US 43674603 A 20030513