

Title (en)

VERTICAL JUNCTION FIELD EFFECT SEMICONDUCTOR DIODES

Title (de)

VERTIKALE SPERRSCHICHT-FELDEFFEKT-HALBLEITERDIODEN

Title (fr)

DIODES A SEMICONDUCTEURS A EFFET DE CHAMP A JONCTION EN STRUCTURE VERTICALE

Publication

EP 1336203 A2 20030820 (EN)

Application

EP 01989323 A 20011018

Priority

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Abstract (en)

[origin: US2002066939A1] Methods and apparatus of forming a semiconductor device using pedestals and sidewalls. The pedestals and sidewalls may provide an etch stop and/or a diffusion barrier during manufacture of a semiconductor device. Processes of forming diode connected vertical cylindrical field effect devices are disclosed to exemplify the use of the pedestals and/or sidewalls. A system for forming the pedestals and sidewalls is described.

IPC 1-7

H01L 29/861; **H01L 21/329**

IPC 8 full level

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CPC (source: EP KR US)

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US 2002066939 A1 20020606; **US 6855614 B2 20050215**; AU 4342702 A 20020624; CA 2428673 A1 20020620; CN 1312779 C 20070425; CN 1531756 A 20040922; EP 1336203 A2 20030820; IL 155782 A0 20031223; IL 155782 A 20071203; JP 2004517473 A 20040610; KR 100812202 B1 20080313; KR 20030070894 A 20030902; TW 520570 B 20030211; US 6580150 B1 20030617; WO 0249116 A2 20020620; WO 0249116 A3 20021128

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