

Title (en)
BALANCED BINARY COLOR DRIVE METHOD AND DIGITALLY CONTROLLED WAVEFORM DRIVE METHODS FOR GRAPHICAL DISPLAYS AND SYSTEM IMPLEMENTING SAME

Title (de)
SYMMETRISCHES BINÄRFARBENANSTEUERVERFAHREN UND DIGITAL GESTEUERTE SIGNALFORMANSTEUERVERFAHREN FÜR GRAFISCHE ANZEIGEN UND DIESE IMPLEMENTIERENDES SYSTEM

Title (fr)
PROCEDE DE COMMANDE A COULEUR BINAIRE EQUILIBREE ET PROCEDES DE COMMANDE A FORME D'ONDE REGULEE NUMERIQUEMENT POUR DES AFFICHAGES GRAPHIQUES ET SYSTEME METTANT EN OEUVRE DE TELS PROCEDES

Publication
EP 1346318 A2 20030924 (EN)

Application
EP 01996011 A 20011129

Priority
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• US 72709500 A 20001129
• US 72713200 A 20001129

Abstract (en)
[origin: WO0245016A2] An image generation system is provided. A display matrix is provided and has a plurality of display elements which can include liquid crystal. Each display element includes a pixel. A plurality of display circuits are electrically connected to a display element. A plurality of memory cells are associated with each of the circuits, or form part of the circuit, and a selector continuously electrically connected to more than one of the plurality of memory cells, the selector outputting to the pixel data from one memory cell at a time. Peripheral control circuits preferably control read and write operations to the memory cells. Also provided is a light emitting mechanism, which may be a mechanism provided at each pixel, a light modulating mechanism provided at each pixel, and/or an illumination source for illuminating the pixels. Logic suppresses image flicker utilizing balanced binary color. A method for generating an image is also provided. A display element of a display is switched to zero volts for a first time duration. A first voltage is supplied to an electrode of the display element for a second period of time. A second voltage is supplied to a counterelectrode of the display element for a third period of time such that no net voltage is applied to the display element. An illumination pulse is applied to the pixel for illuminating the display element for a predetermined number of units of duration where the units of duration can be in fractions of milliseconds, etc.
[origin: WO0245016A2] An image generation system is provided. A display matrix (12) is provided and has a plurality of display elements (14) which can include liquid crystal. Each display element (14) includes a pixel (16). A plurality of display circuits (18) are electrically connected to a display element. A plurality of memory cells (20A, 20B) are associated with each of the circuit. A selector (21) is used to select the pixel data from one memory cell at a time.

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IPC 8 full level
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CPC (source: EP KR)
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See references of WO 0245016A2

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