

Title (en)

DEVICE FOR CONTROLLING A CIRCUIT GENERATING REFERENCE VOLTAGES

Title (de)

STEUERUNGSVORRICHTUNG FÜR EINE REFERENZSPANNUNGSERZEUGSSCHALTUNG

Title (fr)

DISPOSITIF DE COMMANDE D'UN CIRCUIT DE GENERATION DE TENSIONS DE REFERENCE

Publication

**EP 1354255 A1 20031022 (FR)**

Application

**EP 02700385 A 20020123**

Priority

- FR 0200278 W 20020123
- FR 0100953 A 20010124

Abstract (en)

[origin: FR2819954A1] The invention concerns a control device comprising a circuit generating REF reference voltages (VPOL1, V POL2) comprising three P-type MOS transistors (M12, M13 and M14) connected in series between a high-voltage input node (EHV) and the earth (GND), and supplying on the drain and the source of the middle transistor (M13) reference voltages (VPOL1, V POL2). Said device comprises means for controlling the reference transistors either, in a first operating mode, to force the first reference transistor (M12) in current source, the second reference transistor (M13) in off-state and short-circuit the third reference transistor (M14) to the earth, or, in a second operating mode, in connecting each of said transistors in diode, their gate and their drain being connected, on the basis of a logic control signal (/WR). Thus, the resulting reference voltages in output are based on said logic signal.

IPC 1-7

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IPC 8 full level

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CPC (source: EP US)

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