

Title (en)  
DRIVING IC AND OPTICAL PRINT HEAD

Title (de)  
ANSTEUER-IC UND OPTISCHER DRUCKKOPF

Title (fr)  
CIRCUIT DE COMMANDE ET TETE D'IMPRESSION OPTIQUE

Publication  
**EP 1354714 A1 20031022 (EN)**

Application  
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Priority  
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Abstract (en)  
From a latch circuit (12) composed of five stages of latch circuits each storing data signals corresponding to one line, data signals to be fed to a drive circuit (16) to light the corresponding light-emitting sections are selected by a select circuit (15) selects according to correction data, consisting of 9 bits for each light-emitting section, stored in a Y position correction data storage circuit (13). This permits adjustment of the timing with which the individual light-emitting sections are lit and thereby helps achieve printing with satisfactory linearity. <IMAGE>

IPC 1-7  
**G06K 15/12**

IPC 8 full level  
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