

Title (en)

TIMING SYNCHRONIZATION IN A COMMUNICATION DEVICE

Title (de)

ZEITSTEUERUNGSSYNCHRONISATION IN EINEM KOMMUNIKATIONSGERÄT

Title (fr)

SYNCHRONISATION TEMPORELLE DANS UN DISPOSITIF DE COMMUNICATION

Publication

EP 1374471 A1 20040102 (EN)

Application

EP 02719118 A 20020305

Priority

- US 0206628 W 20020305
- US 81370801 A 20010321

Abstract (en)

[origin: WO02078245A1] A method for performing timing synchronization (fig. 3) between digital input data and a system clock in a communication device includes providing a system clock rate that is higher than a data clock rate (34) and sampling the input data at intervals in the system clock periods within the data clock period. The method includes adjusting (36 and 38) the timing between the digital input data and the system clock by either shortening the number of system clock periods and sampling intervals, or lengthening the number of system clock periods and sampling the input data at normal intervals and providing data insertion to fill one of the extra system clock periods, such that more data samples are provided in the data clock period.

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H04L 7/00

IPC 8 full level

H04L 7/00 (2006.01); **H04L 7/02** (2006.01)

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