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INTEGRATED CIRCUIT

Title (de)
INTEGRIERTER SCHALTKREIS

Title (fr)
CIRCUIT INTEGRE

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Abstract (en)
[origin: WO0208964A2] The invention relates to an integrated circuit comprising several adjacent cells, whereby at least two types of cells, which have connections, are provided. According to said method, the cells are of a sufficient size for integrating a large number of logic elements. At least one logic element can be programmed in at least some of the cells and the connections are provided in positions, which at least substantially correspond in the cell, in order to permit a mixture of cell types in the adjacent assembly of cells.

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See references of WO 0208964A2

Citation (examination)

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- US 5963048 A 19991005 - HARRISON DAVID A [US], et al
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