

Title (en)

ACTIVE MATRIX ADDRESSED BISTABLE REFLECTIVE CHOLESTERIC DISPLAYS AND GRAPHIC CONTROLLERS AND OPERATING METHODS THEREFOR

Title (de)

BISTABILE REFLEKTIVE CHOLESTERISCHE ANZEIGEN MIT AKTIVMATRIXADRESSIERUNG UND GRAPHISCHE STEUERUNGEN UND BETRIEBSVERFAHREN DAFÜR

Title (fr)

CONTROLEURS GRAPHIQUES ET AFFICHEURS A CRISTAUX CHOLESTERIQUES REFLECHISSANTS BISTABLES ADRESSES PAR MATRICE ACTIVE ET MODALITES DE MISE EN OEUVRE

Publication

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Application

EP 01968048 A 20010912

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- US 83632901 A 20010418
- US 83664001 A 20010418

Abstract (en)

[origin: WO02086855A1] A low power color display system (1) includes a memory (502), which stores color data and status bits corresponding to a plurality of pixels, status logic (504), which generates the status bits responsive to receipt of color data for a respective one of the pixels, a data generator (520, 522), which generates voltage data corresponding to the pixels based on the color data and the status bits for each of the pixels, driver circuitry (200), which generates voltage signals responsive to receipt of the voltage data for each of the pixels, and a bistable liquid crystal display (LCD) (100) having multiple cells arranged in a matrix, each cell corresponding to a pixel, the LCD being responsive to the voltage signals. Preferably, the system also includes a power supply (400) which provides power to the driver circuitry (200), and a power manager (518), the latter turning the power supply ON when the data generator is in the first operating mode and turning the power supply OFF when the data generator is in the second operating mode. The data generator (520, 522) generates the voltage data for a corresponding pixel N times to thereby permit application of the voltage signal corresponding to the pixel data to the LCD N time, where N is an integer established by the status bits.

IPC 1-7

G09G 3/36; **G09G 5/00**

IPC 8 full level

G09G 3/36 (2006.01); **G09G 5/36** (2006.01)

CPC (source: EP)

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