

Title (en)
ELECTRONIC ISOLATOR

Title (de)
ELEKTRONISCHER ISOLATOR

Title (fr)
ISOLATEUR ELECTRONIQUE

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Application
EP 02746447 A 20020524

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Abstract (en)
[origin: WO02097938A2] The present invention is an electronic isolator (210) that provides low input to output insertion loss, high output to input insertion loss, and substantial asymmetric isolation between a source circuit (200) and a load circuit (220). The invention actively reduces noise and reflected power appearing on the isolator output. In numerous embodiments, the invention operates in circuit applications from dc through millimeter wave. Multistage electronic isolator embodiments (1000) provide increased isolation and greater noise reduction. In other embodiments, the electronic isolator (500) also removes noise appearing on its input. In another embodiment, the invention (1500) is configured for high power applications. This embodiment includes circuitry for redirecting power away from the load into resistors or other dissipative elements. In another embodiment, the electronic isolator (1400) is configured to remove signal distortion produced by one or more power amplifiers in the system.
[origin: WO02097938A2] The present invention is an electronic isolator that provides low input to output insertion loss, high output to input insertion loss, and substantial asymmetric isolation between a source circuit and a load circuit. The invention actively reduces noise and reflected power appearing on the isolator output. In numerous embodiments, the invention operates in circuit applications from dc through millimeter wave. Multistage electronic isolator embodiments provide increased isolation and greater noise reduction. In other embodiments, the electronic isolator also removes noise appearing on its input. In another embodiment, the invention is configured for high power applications. This embodiment includes circuitry for redirecting power away from the load into resistors or other dissipative elements. In another embodiment, the electronic isolator is configured to remove signal distortion produced by one or more power amplifiers in the system.

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• No further relevant documents disclosed
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