

Title (en)
MULTIPLIER CIRCUIT

Title (de)
MULTIPLIZIERERSCHALTUNG

Title (fr)
CIRCUIT MULTIPLICATEUR

Publication
EP 1405413 B1 20050202 (DE)

Application
EP 02752994 A 20020703

Priority
• DE 0202427 W 20020703
• DE 10132802 A 20010706

Abstract (en)
[origin: DE10132802A1] Multiplier circuit comprises: first (1, 2) and second (15, 16) inputs for delivery of first and second input signals; an output (9, 10) for delivery of an output signal dependent on the two inputs; first and second transistor pairs (3, 4; 5, 6) with control inputs that are coupled to the first input and have control paths that are linked to the outputs of the multiplier circuits and at their other ends from a power input; and a current mirror (11, 12) connected on the output side to the transistors pairs and input side to the multiplier circuit inputs.

IPC 1-7
G06F 7/52; **H03J 1/00**

IPC 8 full level
G06F 7/52 (2006.01); **G06G 7/163** (2006.01); **H03J 1/00** (2006.01)

CPC (source: EP US)
G06G 7/163 (2013.01 - EP US)

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DE FR GB

DOCDB simple family (publication)
DE 10132802 A1 20021114; DE 50202182 D1 20050310; EP 1405413 A2 20040407; EP 1405413 B1 20050202; US 2004174199 A1 20040909; WO 03005582 A2 20030116; WO 03005582 A3 20030313

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