

Title (en)
DIGITAL CIRCUITS WITH SELECTION OPERATORS

Title (de)
DIGITALE SCHALTUNGEN MIT SELEKTIONSOPERATOREN

Title (fr)
CIRCUITS NUMERIQUES A OPERATEURS DE SELECTION

Publication
EP 1407392 A4 20060614 (EN)

Application
EP 02744461 A 20020617

Priority

- US 0219488 W 20020617
- US 29883201 P 20010615
- US 17274302 A 20020614
- US 17274402 A 20020614
- US 17249402 A 20020614
- US 17274202 A 20020614
- US 17274602 A 20020614
- US 17274502 A 20020614

Abstract (en)
[origin: WO02103757A2] A digital integrated circuit chip is designed by identifying a logical structure to be implemented. This logical structure is represented in terms of a logical operations, at least 5% of which include selection operations. A determination is made of logic cells that correspond to an implementation of these logical operations.

IPC 1-7
G06F 17/50

IPC 8 full level
G06F 17/50 (2006.01)

CPC (source: EP US)
G06F 30/30 (2020.01 - EP US); **G06F 30/327** (2020.01 - EP US)

Citation (search report)

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- [X] HERNANDEZ-AGUIRRE A ET AL: "A genetic programming approach to logic function synthesis by means of multiplexers", EVOLVABLE HARDWARE, 1999. PROCEEDINGS OF THE FIRST NASA/DOD WORKSHOP ON PASADENA, CA, USA 19-21 JULY 1999, LOS ALAMITOS, CA, USA,IEEE COMPUT. SOC, US, 19 July 1999 (1999-07-19), pages 46 - 53, XP010346253, ISBN: 0-7695-0256-3
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- See references of WO 02103757A2

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WO 02103757 A2 20021227; WO 02103757 A3 20031106; AU 2002344835 A1 20030102; CN 1541364 A 20041027; EP 1407392 A2 20040414; EP 1407392 A4 20060614

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