

Title (en)

MEMORY AND METHOD FOR AN IMPROVED SENSING

Title (de)

HALBLEITERSPEICHER MIT VERBESSERTER LESEANORDNUNG SOWIE ZUGEHÖRIGE BETRIEBSART

Title (fr)

MEMOIRE A SEMI-CONDUCTEUR AVEC ENSEMBLE DE LECTURE AMELIORE, ET SON MODE DE FONCTIONNEMENT

Publication

**EP 1412949 A1 20040428 (DE)**

Application

**EP 02754426 A 20020724**

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Abstract (en)

[origin: WO03015103A1] A selection transistor (2) for a group of memory cells, preferably 16-32 memory cells, is respectively introduced into the feed lines to the memory cells (4). The selection transistor is opened to a line group for reading, while the control gates of all lines are at low potential and the current for each reading column leading through said line group is measured and stored. In a second step, the control gate (5) of the line to be read is brought to a higher reading potential and the resulting current is compared to the previous current.

IPC 1-7

**G11C 16/26; G11C 7/14**

IPC 8 full level

**G11C 16/06** (2006.01); **G11C 16/26** (2006.01)

CPC (source: EP US)

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DOCDB simple family (publication)

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