

Title (en)
TRANSMISSION OF LARGE VOLUMES OF DATA VIA ASYNCHRONOUS INTERFACES IN CIRCUITS WITH REDUNDANCY CONCEPT OF THE CHECKER-MASTER TYPE

Title (de)
ÜBERTRAGUNG GROSSER DATENMENGEN ÜBER ASYNCHRONE SCHNITTSTELLEN IN SCHALTUNGEN MIT MASTER-CHECKER-REDUNDANZKONZEPT

Title (fr)
TRANSMISSION DE GROS VOLUMES DE DONNEES PAR L'INTERMEDIAIRE D'INTERFACES ASYNCHRONES DANS DES CIRCUITS A CONCEPT DE REDONDANCE DE TYPE CONTROLEUR-MAITRE

Publication
EP 1421492 A2 20040526 (DE)

Application
EP 02776659 A 20020828

Priority
• DE 0203155 W 20020828
• DE 10142611 A 20010831

Abstract (en)
[origin: WO03026175A2] The invention concerns a method for transmitting large volumes of data via an asynchronous interface in a redundancy circuit of the checker-master type. The invention is characterized in that the data are transmitted on the basis of the type and volume of data to be transmitted, in accordance with different transmission cycles, whereof the operation is fixed and provided by means of finite state automats (FSM) on both sides of the interface. Said FSM can control the management of the data on data buses whereof the number and size are random. On each of said buses, the data are stably maintained on the output side until they can be securely managed on the input side. Said concept enable transmission of a random number of data at every following phase until the cycle is completed.

IPC 1-7
G06F 11/18

IPC 8 full level
G06F 11/16 (2006.01)

CPC (source: EP US)
G06F 11/1679 (2013.01 - EP US)

Citation (search report)
See references of WO 03026175A2

Designated contracting state (EPC)
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LI LU MC NL PT SE SK TR

DOCDB simple family (publication)
WO 03026175 A2 20030327; WO 03026175 A3 20030703; CN 1549970 A 20041124; EP 1421492 A2 20040526; US 2004208202 A1 20041021

DOCDB simple family (application)
DE 0203155 W 20020828; CN 02817018 A 20020828; EP 02776659 A 20020828; US 48832104 A 20040227