

Title (en)

REQUIREMENTS FOR DYNAMIC RATE REPARTITIONING

Title (de)

ANFORDERUNGEN FÜR DYNAMISCHEN RATEREPARTITIONIERUNG

Title (fr)

BESOINS EN REPARTITION DYNAMIQUE DU DEBIT

Publication

**EP 1425895 A2 20040609 (EN)**

Application

**EP 02770490 A 20020910**

Priority

- US 0228722 W 20020910
- US 31847001 P 20010910

Abstract (en)

[origin: WO03024147A2] The invention provides a system and method for enabling DRR that supports multiple, simultaneous voice call events. In addition the invention provides for DRR that support the activation and deactivation of bit synchronous PCM timeslots within a well-defined period of time (e.g., not to exceed 100 ms) that can accomodate any voice call control signaling procedures, DRR that provides for robust communication of the DRR signaling protocol between the line terminal (LT) and the network terminal (NT), DRR that supports periodically refreshing the status of the bit synchronous PCM timeslots and DRR that aligns with proposed DSL bearer requirements.

IPC 1-7

**H04L 29/08**; **H04L 12/64**

IPC 8 full level

**H04L 29/08** (2006.01); **H04L 12/64** (2006.01); **H04Q 11/04** (2006.01)

CPC (source: EP KR)

**H04L 12/16** (2013.01 - KR); **H04Q 11/04** (2013.01 - EP); **H04Q 2213/13031** (2013.01 - EP); **H04Q 2213/13039** (2013.01 - EP); **H04Q 2213/13292** (2013.01 - EP); **H04Q 2213/13332** (2013.01 - EP)

Citation (search report)

See references of WO 03024147A2

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LI LU MC NL PT SE SK TR

DOCDB simple family (publication)

**WO 03024147 A2 20030320**; **WO 03024147 A3 20040304**; AU 2002335727 A1 20030324; EP 1425895 A2 20040609; JP 2005505157 A 20050217; KR 20040033305 A 20040421

DOCDB simple family (application)

**US 0228722 W 20020910**; AU 2002335727 A 20020910; EP 02770490 A 20020910; JP 2003528057 A 20020910; KR 20047003582 A 20020910