Title (en)

Measurement architecture to obtain performance characteristics in packet-based networks

Title (de)

Messarchitektur zur Ermittlung der Leistungscharakteristik in paketbasierten Netzwerken

Title (fr)

Architecture de mesure pour obtenir des caractéristiques de performance dans des réseaux à commutation par paquets

Publication

EP 1427137 B1 20130109 (EN)

Application

EP 03024774 A 20031030

Priority

US 30713302 A 20021129

Abstract (en)

[origin: EP1427137A2] An architecture for measurement of per-hop, one-way delay includes a input observation circuit (24) at the input interface of a node (12) and a output observation circuit (26) at the output interface (16) of a node (12). The input observation circuit (24) copies and time stamps the headers of incoming packets, and filters packets according to an aggregate definition. Similarly, the output observation circuit (26) copies and time stamps the headers of outgoing packets, and filters packets according to the aggregate definition. The incoming and outgoing traces are correlated to calculate a delay measurement. A packet loss measurement uses input observation circuits (72a and 72b) at the input interfaces (14) of upstream and downstream nodes (20 and 22) and an output observation circuit 74 at the output interface (16) of the upstream node. The observation circuits (72a, 72b and 74) determine the number of lost packets across a node and between nodes, according to an aggregate definition.

IPC 8 full level

H04L 12/26 (2006.01); **H04L 12/56** (2006.01)

CPC (source: EP US)

H04L 43/0829 (2013.01 - EP US); **H04L 43/0852** (2013.01 - EP US); **H04L 43/106** (2013.01 - EP US)

Cited by

WO2010084151A1; EP2211508A1; EP2487836A1; CN103384986A; EP4113945A1; US11757751B2; US8000251B2; WO2012104326A1; WO2009064651A1; US8612625B2; US9413625B2; US11818029B2

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DOCDB simple family (publication)

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