

Title (en)
DEMODULATION CIRCUIT

Title (de)
SCHALTUNG ZUR DEMODULATION

Title (fr)
CIRCUIT DE DEMODULATION

Publication
EP 1427997 A1 20040616 (DE)

Application
EP 02779108 A 20020919

Priority
• DE 0203508 W 20020919
• DE 10146286 A 20010919

Abstract (en)
[origin: WO03025515A1] The invention relates to a circuit for demodulating at least one modulated signal (e), preferably a measuring signal of a sensor, said circuit comprising at least one input (1), to which the signal (e) is applied. In order to permit an application of said circuit even with limited available space for the circuit, said demodulation circuit is designed, such that the demodulation of said signal (e) is executed by means of at least one switched-capacitor network (2), connected to said input (1). The present invention also relates to a corresponding method.

IPC 1-7
G01D 5/20; H03H 19/00

IPC 8 full level
H03H 19/00 (2006.01); **G01D 5/20** (2006.01)

CPC (source: EP US)
G01D 5/202 (2013.01 - EP US)

Citation (search report)
See references of WO 03025515A1

Designated contracting state (EPC)
AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LI LU MC NL PT SE SK TR

DOCDB simple family (publication)
WO 03025515 A1 20030327; DE 10243632 A1 20030417; EP 1427997 A1 20040616; JP 2005508589 A 20050331; JP 4564258 B2 20101020; US 2004071245 A1 20040415; US 7012462 B2 20060314

DOCDB simple family (application)
DE 0203508 W 20020919; DE 10243632 A 20020919; EP 02779108 A 20020919; JP 2003529097 A 20020919; US 64895703 A 20030827