

Title (en)

ELECTRONIC CIRCUIT ASSEMBLY COMPRISING AT LEAST A STORAGE UNIT WITH ERROR CORRECTING MEANS

Title (de)

SCHALTKREISKOMPLEX DAS MINDESTENS EINEN SPEICHER UMFASST MIT FEHLERKORREKTURMITTELN

Title (fr)

ENSEMBLE DE CIRCUITS ELECTRONIQUES COMPORTANT AU MOINS UNE MEMOIRE AVEC DES MOYENS DE CORRECTION D'ERREUR

Publication

EP 1428121 A2 20040616 (FR)

Application

EP 02794620 A 20020802

Priority

- FR 0202785 W 20020802
- FR 0110735 A 20010810

Abstract (en)

[origin: FR2828566A1] Assembly of electronic circuits comprising at least a static memory (1) and means (5, 6) for error detection and correction of data supplied by the memory during a read operation, with the memory being connected in series with a plurality of successive error correction stages. The error correction stages are short-circuited if an error is not detected. Error correction circuits are connected in stages. Thus if an error is corrected in a first stage, subsequent stages are blocked, and so on.

IPC 1-7

G06F 11/00

IPC 8 full level

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CPC (source: EP US)

G06F 11/1048 (2013.01 - EP US); **G06F 11/1052** (2013.01 - EP US)

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