Title (en)

USER EQUIPMENT (UE) HAVING A HYBRID PARALLEL/SERIAL BUS INTERFACE

Title (de)

BENUTZERGERÄTE (UE) MIT EINER HYBRIDEN PARALLELEN/SERIELLEN BUSSCHNITTSTELLE

Title (fr)

EQUIPEMENT UTILISATEUR (UE) COMPRENANT UNE INTERFACE DE BUS PARALLELE/SERIE HYBRIDE

Publication

EP 1446722 A4 20050420 (EN)

Application

EP 02789726 A 20021118

Priority

- US 0236954 W 20021118
- US 99006001 A 20011121
- US 8089902 A 20020222

Abstract (en)

[origin: WO03046737A1] A hybrid serial/parallel bus interface for a user equipment (UE) has a data block demultiplexing device (40). The data block demultiplexing device has an input configured to receive a data block and demultiplexes the data block into a plurality of nibbles. For each nibble, a parallel to serial converter (42) converts the nibble into serial data. A line (44) transfers each nibble's serial data. A serial to parallel converter (46) converts each nibble's serial data to recover that nibble. A data block reconstruction device (48) combines the recovered nibbles into the data block.

IPC 1-7

H03M 9/00; G06F 13/00

IPC 8 full level

G06F 13/38 (2006.01); **H03M 9/00** (2006.01); **H04L 25/14** (2006.01); **H04L 29/00** (2006.01)

CPC (source: EP)

H03M 9/00 (2013.01); **H04L 25/14** (2013.01)

Citation (search report)

- [XY] "DS90CR211/DS90CR212 21-Bit Channel Link", July 1997, NATIONAL SEMICONDUCTOR, SANTA CLARA, CALIFORNIA, USA, XP002306540
- [X] NOVAK T ET AL: "Channel Link - Moving and Shaping Information In Point-toPoint Applications", May 1996, NATIONAL SEMICONDUCTOR, SANTA CLARA, CALIFORNIA, USA, XP002306541
- [X] VON HERZEN B ET AL: "Multi-Channel 622 Mb/s LVDS Data Transfer for Virtex-E Devices", 6 January 2001, XILINX INC., SAN JOSE, CALIFORNIA, USA, XP002306542
- [A] KITANOWSKA S ET AL: "Bus LVDS with Virtex-E Devices", 26 July 2000, XILINX INC, SAN JOSE, CALIFORNIA, USA, XP002307032
- [X] MAC GETTIGAN E: "Eight Channel, One Clock, One Frame LVDS Transmitter/Receiver", 15 March 2001, XILINX INC, SAN JOSE, CALIFORNIA, USA, XP002317897
- [Y] HUQ S B ET AL: "An Overview of LVDS Technology", July 1998, NATIONAL SEMICONDUCTOR, SANTA CLARA, CALIFORNIA, USA, XP002317898
- [Y] "3606 - Digitally Controlled Programmable Gain Instrumentation Amplifier", October 1983, BURR-BROWN CORPORATION, TUCSON, ARIZONA, USA, XP002317899

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR IE IT LI LU MC NL PT SE SK TR

DOCDB simple family (publication)

WO 03046737 A1 20030605; AT E388525 T1 20080315; AT E397323 T1 20080615; AU 2002352773 A1 20030610; CA 2467841 A1 20030605; CA 2467841 C 20080513; CN 100346327 C 20071031; CN 1589437 A 20050302; DE 60226910 D1 20080710; EP 1446722 A1 20040818; EP 1446722 A4 20050420; HK 1069905 A1 20050603; JP 2005510800 A 20050421; MX PA04004742 A 20040802; NO 20042522 L 20040616; TW 200402240 A 20040201; TW 200419359 A 20041001; TW I260172 B 20060811; TW I285316 B 20070811

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