

Title (en)
RECONFIGURABLE SYSTEM

Title (de)
REKONFIGURIERBARES SYSTEM

Title (fr)
SYSTEME RECONFIGURABLE

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Abstract (en)
[origin: WO03025770A2] The invention relates to a reconfigurable processor (VPU) which is designed to be integrated into a technical environment comprising a standard processor (CPU), such as a DSP, RISC, or CISC processor or a (micro)controller. Said processor must be designed in such a way that a connection is created which is as simple and as efficient as possible. According to another aspect of the invention, the system created is easy to programme. The invention takes into account the continued use of existing programmes of the CPU, the code compatibility and the simple integration of the VPU into the existing programmes.

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