Title (en)

METHOD FOR DETERMINATION OF A SEPARATION FROM PROCESSOR UNITS TO AT LEAST ONE REFERENCE POSITION IN A PROCESSOR ARRANGEMENT AND PROCESSOR ARRANGEMENT

Title (de)

VERFAHREN ZUM BESTIMMEN EINES ABSTANDS VON PROZESSOREINHEITEN ZU MINDESTENS EINER REFERENZPOSITION IN EINER PROZESSOR-ANORDNUNG UND PROZESSOR-ANORDNUNG

Title (fr)

PROCEDE POUR DETERMINER L'ECART D'UNITES PROCESSEURS PAR RAPPORT A AU MOINS UNE POSITION DE REFERENCE DANS UN ENSEMBLE DE PROCESSEURS, ET ENSEMBLE DE PROCESSEURS

Publication

**EP 1449110 A2 20040825 (DE)**

Application

**EP 02798255 A 20021128**

Priority

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Abstract (en)

[origin: WO03048953A2] A processor arrangement comprises a number of processor units. Each processor unit is coupled to at least one adjacent processor unit by means of a bi-directional communication interface. Messages are exchanged between adjacent processor units for the determination of the separation of a processor unit in the processor arrangement from a reference position. Each message contains separation information, giving the separation of the processor unit receiving the message from the reference position or the separation of the processor unit sending the message from the reference position, where each processor unit is embodied such as to be able to determine or store the separation thereof from the reference position from the separation information in a received message.

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**G06F 15/80**

IPC 8 full level

**G06F 15/80** (2006.01); **G09G 3/20** (2006.01)

CPC (source: EP US)

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