

Title (en)

SYSTEMATIC OPTIMISATION OF INDIVIDUAL BLOCKS IN HIGH-SPEED CMOS CIRCUITS

Title (de)

SYSTEMATISCHE OPTIMIERUNG VON EINZELBLÖCKEN IN HOCHGESCHWINDIGKEITSCMOSSCHALTUNGEN

Title (fr)

CONCEPTION D'UN PROCEDE ET DE CIRCUITS PERMETTANT D'EFFECTUER DES REGLAGES SYSTEMATIQUES DANS DES CIRCUITS CMOS A HAUTE VITESSE

Publication

EP 1449301 A1 20040825 (EN)

Application

EP 02746801 A 20020626

Priority

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- US 13798802 A 20020502

Abstract (en)

[origin: TW567670B] Techniques for designing an electronic circuit system including multiple transistors and passive components are presented. According to one aspect of the techniques, some or all of the transistors and passive components are systematically adjusted to minimize artifacts resulting from system-level interactions among these functional building blocks. The adjustment is based on a ratio of electrically equivalent channel geometry (EECG) of each of the adjusted the transistors and passive components.

IPC 1-7

H03K 19/00

IPC 8 full level

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Citation (examination)

- US 5196805 A 19930323 - BECKWITH WILLIAM B [US], et al
- US 5945847 A 19990831 - RANSIJN JOHANNES GERARDUS [US]
- US 4806796 A 19890221 - BUSHEY THOMAS P [US], et al
- US 4289979 A 19810915 - MULLER HAROLD

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