

Title (en)

INTEGRATED CIRCUIT COMPRISING A CLOCK GENERATOR, A CHIP CARD COMPRISING ONE SUCH INTEGRATED CIRCUIT AND THE ASSOCIATED CLOCK GENERATION METHOD

Title (de)

INTEGRIERTE SCHALTUNG MIT EINEM TAKTGENERATOR, CHIPKARTE MIT EINER SOLCHEN INTEGRIERTEN SCHALTUNG UND VERFAHREN ZUM GENERIEREN EINES ZUGEHÖRIGES TAKTSIGNALS

Title (fr)

CIRCUIT INTEGRE COMPRENANT UN GENERATEUR D'HORLOGE, CARTE A PUCE COMPRENANT UN TEL CIRCUIT INTEGRE ET PROCEDE DE GENERATION D'HORLOGE ASSOCIE

Publication

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Application

EP 02796907 A 20021211

Priority

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Abstract (en)

[origin: WO03050955A2] The invention relates to a contactless integrated circuit which receives a radio frequency signal. According to the invention, the circuit comprises a clock generator, said generator being used to produce a clock signal (CLK) from a first alternation (AC0) and a second alternation (AC1) which are representative of the radio frequency signal received. The invention also relates to a method of generating a clock signal, during which the first and the second alternations are compared in order to produce the clock signal. The invention is suitable for contactless cards, transponders, etc.

IPC 1-7

H03K 5/125

IPC 8 full level

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CPC (source: EP US)

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