

Title (en)
Voltage controlled oscillator

Title (de)
Spannungsgesteuerter Oszillator

Title (fr)
Oscillateur commandé en tension

Publication
EP 1463196 B1 20051207 (FR)

Application
EP 04290694 A 20040315

Priority
FR 0303898 A 20030328

Abstract (en)
[origin: EP1463196A1] The oscillator has two CMOS inverters (14, 16) coupled to form a quadripole with two inputs and two outputs. Two oscillatory circuits are laid out between respective inputs and outputs of the CMOS inverters and include an inductance coil each. The quadripole is adapted such that the outputs of the quadripole are in phase. The oscillation circuits are formed in MOS technology and are superimposed on one another.

IPC 1-7
H03B 5/12

IPC 8 full level
H01L 21/822 (2006.01); **H01L 27/04** (2006.01); **H03B 5/12** (2006.01); **H03K 3/03** (2006.01); **H03K 3/354** (2006.01)

CPC (source: EP US)
H03B 5/1212 (2013.01 - EP US); **H03B 5/1218** (2013.01 - EP US); **H03B 5/1228** (2013.01 - EP US); **H03B 5/124** (2013.01 - EP US);
H03B 5/1296 (2013.01 - EP US); **H03K 3/03** (2013.01 - EP US); **H03K 3/354** (2013.01 - EP US)

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AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LI LU MC NL PL PT RO SE SI SK TR

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EP 1463196 A1 20040929; EP 1463196 B1 20051207; AT E312431 T1 20051215; DE 602004000213 D1 20060112;
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