

Title (en)

SIGNAL VALIDATION AND ARBITRATION SYSTEM AND METHOD

Title (de)

VERFAHREN UND SYSTEM ZUR SIGNALBESTÄTIGUNG UND ARBITRIERUNG

Title (fr)

SYSTEME ET PROCEDE DE VALIDATION ET D'ARBITRAGE DE SIGNAUX

Publication

**EP 1468337 A1 20041020 (EN)**

Application

**EP 03708864 A 20030122**

Priority

- US 0301994 W 20030122
- US 23783402 A 20020909

Abstract (en)

[origin: WO03062932A1] A common design framework for input and output signal validation, arbitration, and fault reporting for real-time controllers includes a method of validating redundant input and output signals and arbitrating between the redundant input and output signals by determining a fault severity level for each of the redundant input and output signals, and determining a signal to transmit for further processing based at least in part on the determined fault severity levels.

IPC 1-7

**G05B 9/03**

IPC 8 full level

**H04L 1/06** (2006.01); **H04L 1/22** (2006.01); **H04L 1/24** (2006.01)

CPC (source: EP)

**H04L 1/06** (2013.01); **H04L 1/22** (2013.01); **H04L 1/24** (2013.01)

Citation (search report)

See references of WO 03062932A1

Designated contracting state (EPC)

FR GB

DOCDB simple family (publication)

**WO 03062932 A1 20030731**; EP 1468337 A1 20041020

DOCDB simple family (application)

**US 0301994 W 20030122**; EP 03708864 A 20030122