

Title (en)

SIGNAL VALIDATION AND ARBITRATION SYSTEM AND METHOD

Title (de)

VERFAHREN UND SYSTEM ZUR SIGNALBESTÄTIGUNG UND ARBITRIERUNG

Title (fr)

SYSTEME ET PROCEDE DE VALIDATION ET D'ARBITRAGE DE SIGNAUX

Publication

EP 1468337 A1 20041020 (EN)

Application

EP 03708864 A 20030122

Priority

- US 0301994 W 20030122
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Abstract (en)

[origin: WO03062932A1] A common design framework for input and output signal validation, arbitration, and fault reporting for real-time controllers includes a method of validating redundant input and output signals and arbitrating between the redundant input and output signals by determining a fault severity level for each of the redundant input and output signals, and determining a signal to transmit for further processing based at least in part on the determined fault severity levels.

IPC 1-7

G05B 9/03

IPC 8 full level

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