

Title (en)  
Fault tolerant Information processing apparatus

Title (de)  
Fehlertolerantes Datenverarbeitungssystem

Title (fr)  
Système de traitement de données tolérant aux fautes

Publication  
**EP 1469390 A2 20041020 (EN)**

Application  
**EP 04009029 A 20040415**

Priority  
JP 2003114385 A 20030418

Abstract (en)  
An information processing apparatus includes two first and second CPU modules (100, 200) which perform the same process in synchronism with each other and an I/O module (300; 400) connected to the CPU modules (100, 200). The I/O module (300, 400) receives output data of the two CPU modules (100, 200) and compares a plurality of output data of the first CPU module (100) and a plurality of output data of the second CPU module (200) with each other with the output data adjusted in order so as to correspond to each other to discriminate whether or not the output data coincide with each other.

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**G06F 11/16**; **G06F 11/28**

IPC 8 full level  
**G06F 11/16** (2006.01); **G06F 11/18** (2006.01); **G06F 13/00** (2006.01)

CPC (source: EP US)  
**G06F 11/1691** (2013.01 - EP US); **G06F 11/1679** (2013.01 - EP US)

Cited by  
FR2925191A1; JP2009505185A; GB2415805A; EP1675004A3; US8196027B2

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**EP 04009029 A 20040415**; AU 2004201590 A 20040416; CN 200410036826 A 20040419; JP 2003114385 A 20030418; US 82459404 A 20040415