

Title (en)

Fault tolerant Information processing apparatus

Title (de)

Fehlertolerantes Datenverarbeitungssystem

Title (fr)

Système de traitement de données tolérant aux fautes

Publication

EP 1469390 A2 20041020 (EN)

Application

EP 04009029 A 20040415

Priority

JP 2003114385 A 20030418

Abstract (en)

An information processing apparatus includes two first and second CPU modules (100, 200) which perform the same process in synchronism with each other and an I/O module (300; 400) connected to the CPU modules (100, 200). The I/O module (300, 400) receives output data of the two CPU modules (100, 200) and compares a plurality of output data of the first CPU module (100) and a plurality of output data of the second CPU module (200) with each other with the output data adjusted in order so as to correspond to each other to discriminate whether or not the output data coincide with each other.

IPC 1-7

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IPC 8 full level

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CPC (source: EP US)

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Cited by

FR2925191A1; JP2009505185A; GB2415805A; EP1675004A3; US8196027B2

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EP 1469390 A2 20041020; **EP 1469390 A3 20060118**; AU 2004201590 A1 20041104; CN 1538299 A 20041020; JP 2004318702 A 20041111; JP 4155088 B2 20080924; US 2004221195 A1 20041104

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