

Title (en)

INTEGRATED SYSTEM FOR PROCESSING SEMICONDUCTOR WAFERS

Title (de)

INTEGRIERTES SYSTEM ZUR BEARBEITUNG VON HALBLEITERSCHEIBEN

Title (fr)

SYSTEME INTEGRE DE TRAITEMENT DE PLAQUETTES A SEMI-CONDUCTEURS

Publication

**EP 1474251 A2 20041110 (EN)**

Application

**EP 03739583 A 20030217**

Priority

- GB 0300695 W 20030217
- US 35714802 P 20020215
- US 39774002 P 20020720

Abstract (en)

[origin: WO03068421A2] An integrated process tool for chemical mechanical processing, cleaning and drying a semiconductor workpiece is provided. The integrated process tool includes a CMP module and a cleaning and drying module. After being processed, the workpiece is transported from the CMP module to the cleaning and drying module using a movable housing. In the cleaning and drying module, a cleaning mechanism is used to clean the workpiece while the workpiece is rotated and held by a support structure of the movable housing. A drying mechanism of the cleaning and drying module picks up the workpiece from the moveable housing and spin dries it. Throughout the CMP process, cleaning and drying, the processed surface of the wafer faces down.

IPC 1-7

**B08B 1/00**

IPC 8 full level

**G05B 19/418** (2006.01); **H01L 21/00** (2006.01); **H01L 21/02** (2006.01); **H01L 21/304** (2006.01); **H01L 21/677** (2006.01); **H01L 21/687** (2006.01)

CPC (source: EP KR)

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**H01L 21/67173** (2013.01 - EP); **H01L 21/67745** (2013.01 - EP); **H01L 21/68707** (2013.01 - EP); **H01L 21/68728** (2013.01 - EP);  
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