

Title (en)
LOW-PROCESSOR-LOAD AGGREGATION

Title (de)
AGGREGATION MIT NIEDRIGER PROZESSORLAST

Title (fr)
AGREGATION A FAIBLE CHARGEMENT DE PROCESSEUR

Publication
EP 1476818 A1 20041117 (EN)

Application
EP 03705893 A 20030121

Priority
• US 0302067 W 20030121
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Abstract (en)
[origin: WO03063011A1] A system includes a low-processor-load aggregation device (250) having (a) at least one multi-channel device (218, 228, 238), the multi-channel device (218, 228, 238) having at least one internal network tag associated with at least one network station (108, 112), and (b) a host-processor-controlled routing device (204) operably coupled with the internal network (102), the host-processor-controlled routing device (204) configured to coordinate at least one network address with the internal network tag (s) associated with the network station (s) (108, 112). Other embodiments include methods for coordinating an external network-station packet with an internal private-network address of a low-processor-load aggregation device (250), and coordinating an external network-station control message with an internal private-network address of a low-processor-load aggregation device

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IPC 8 full level
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