Title (en)
  DSP ASSISTED PEAK CAPTURE CIRCUIT AND METHOD

Title (de)
  DSP-GESTÜTZTER SPITZENFANGKREIS UND VERFAHREN

Title (fr)
  CIRCUIT ET PROCEDE DE CAPTAGE DE CRETES ASSISTE PAR TRAITEMENT NUMERIQUE DU SIGNAL

Publication
  EP 1481254 A1 20041201 (EN)

Application
  EP 03705977 A 20030131

Priority
  • US 0302711 W 20030131
  • US 35258502 P 20020131

Abstract (en)
  [origin: WO03065060A1] Peak capture circuitry for measuring a peak value of a waveform characterized by a first portion having a first rate of change and a second portion having a relatively faster rate of change. Peak stretching circuitry stretches a portion of the waveform as a function of time. Digital signal processing circuitry samples an output waveform of the peak stretching circuitry for determining the peak value. Thus, despite any steeply sloped portion, samples can be reliably taken throughout the waveform for calculation of peak value.

IPC 1-7
  G01R 19/25

IPC 8 full level
  G01R 19/04 (2006.01); **G01R 19/25** (2006.01); **G06F 1/03** (2006.01); **H03K 5/153** (2006.01); **H03K 5/1532** (2006.01); **H03K 17/00** (2006.01)

CPC (source: EP US)
  G01R 19/04 (2013.01 - EP US); G01R 19/2506 (2013.01 - EP US)

Citation (search report)
  See references of WO 03065060A1

Designated contracting state (EPC)
  AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LI LU MC NL PT SE SI SK TR

DOCDB simple family (publication)
  WO 03065060 A1 20030807; CA 2473580 A1 20030807; EP 1481254 A1 20041201; JP 2005516224 A 20050602; NZ 534058 A 20070427; US 2003163265 A1 20030828; US 6879919 B2 20050412

DOCDB simple family (application)
  US 0302711 W 20030131; CA 2473580 A 20030131; EP 03705977 A 20030131; JP 2003564601 A 20030131; NZ 53405803 A 20030131; US 35526703 A 20030131