Title (en)
METHOD AND ARRANGEMENT FOR POWER EFFICIENT CONTROL OF PROCESSORS

Title (de)
VERFAHREN UND ANORDNUNG ZUR LEISTUNGSEFFIZIENTEN STEUERUNG VON PROZESSOREN

Title (fr)
PROCEDE ET DISPOSITIF POUR COMMANDER DES PROCESSEURS AVEC UN RENDEMENT ENERGETIQUE ELEVE

Publication
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Application
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Abstract (en)
[origin: WO03096184A2] The invention relates to a method for the functional control of program and/or data flows in digital signal processors and processors which have respective closed and separated modules for program and data flow control, working in parallel with computers. The aim of the invention is to carry out a power-efficient adaptation of the signal process with the applied SIMD command-type in the individual paths and minimize the emergence of the appearance of NOP-commands with which the VLIW-architecture of the processor must be supplied. This is achieved by individually controlling the parallel signal processing of the processor in the data paths (DP) which respectively belong to the first and second slice. This is carried out by causing a single slice halt outputted from an SSM register bank to switch the register clockline according to state-dependent signal processing.

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