

Title (en)

A SURFACE GEOMETRY FOR MOS-GATED DEVICE

Title (de)

OBERFLÄCHENGEOMETRIE FÜR MOS-GATED-BAUELEMENTE

Title (fr)

GEOMETRIE DE SURFACE POUR DISPOSITIF A GRILLE MOS

Publication

EP 1504467 A1 20050209 (EN)

Application

EP 03731142 A 20030509

Priority

- US 0314626 W 20030509
- US 14260002 A 20020510
- US 14262202 A 20020510

Abstract (en)

[origin: WO03096406A1] A surface geometry for a MOS-gated device is provided that allows device size to be varied in both the x-axis and the y-axis by predetermined increments. The actual device size is set or "programmed" by the metal and pad masks or the contact metal and pad masks. This approach saves both time and expense. A 7X9 array (11) of identical tiles (13) is shown.

IPC 1-7

H01L 21/336; H01L 29/76

IPC 8 full level

H01L 21/336 (2006.01); **H01L 21/76** (2006.01); **H01L 27/02** (2006.01); **H01L 29/06** (2006.01); **H01L 29/78** (2006.01); **H01L 29/423** (2006.01)

CPC (source: EP US)

H01L 27/0207 (2013.01 - EP); **H01L 29/0696** (2013.01 - EP); **H01L 29/4238** (2013.01 - EP); **H01L 29/7811** (2013.01 - EP US);
H01L 29/7813 (2013.01 - EP US); **H01L 29/402** (2013.01 - EP); **H01L 29/407** (2013.01 - EP); **H01L 2924/0002** (2013.01 - EP)

C-Set (source: EP)

H01L 2924/0002 + H01L 2924/00

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