

Title (en)

METHOD AND DEVICE FOR DATA PROCESSING

Title (de)

VERFAHREN UND VORRICHTUNG ZUR DATENVERARBEITUNG

Title (fr)

PROCEDE ET DISPOSITIF DE TRAITEMENT DE DONNEES

Publication

**EP 1518186 A2 20050330 (DE)**

Application

**EP 03720231 A 20030321**

Priority

- EP 03720231 A 20030321
- DE 0300942 W 20030321
- DE 10212622 A 20020321
- DE 10212621 A 20020321
- DE 10219681 A 20020502
- DE 10226186 A 20020612
- DE 10227650 A 20020620
- DE 10236271 A 20020807
- DE 10236272 A 20020807
- DE 10236269 A 20020807
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- DE 10238172 A 20020821
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- DE 10241812 A 20020906
- DE 10300380 A 20030107
- DE 0300152 W 20030120
- DE 0300489 W 20030218
- EP 02009868 A 20020502
- EP 0206865 W 20020620
- EP 0210065 W 20020816
- EP 0210479 W 20020918
- EP 0210464 W 20020918
- EP 0210572 W 20020919
- EP 02022692 A 20021010
- EP 02027277 A 20021206
- EP 0300624 W 20030120

Abstract (en)

[origin: WO03081454A2] The invention describes how the coupling of a conventional processor, more particularly a sequential processor, and a reconfigurable field of data processing units, more particularly a runtime reconfigurable field of data processing units, can be embodied.

IPC 1-7

**G06F 15/78**

IPC 8 full level

**G06F 15/78** (2006.01); **G06F 9/445** (2006.01); **G06F 12/08** (2006.01); **G06F 12/084** (2016.01)

CPC (source: EP US)

**G06F 9/3877** (2013.01 - EP US); **G06F 9/3897** (2013.01 - EP US); **G06F 12/084** (2013.01 - EP US); **G06F 13/4221** (2013.01 - EP US); **G06F 15/7867** (2013.01 - EP US); **G06F 2212/621** (2013.01 - US)

Citation (search report)

See references of WO 03081454A2

Citation (examination)

ZHIYUAN LI ET AL: "Configuration prefetching techniques for partial reconfigurable coprocessor with relocation and defragmentation", INTERNATIONAL SYMPOSIUM ON FIELD PROGRAMMABLE GATE ARRAYS, XX, XX, 1 February 2002 (2002-02-01), pages 187 - 195, XP002349321

Designated contracting state (EPC)

AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LI LU MC NL PT RO SE SI SK TR

DOCDB simple family (publication)

**WO 03081454 A2 20031002**; **WO 03081454 A3 20050127**; **WO 03081454 A8 20040212**; AU 2003223892 A1 20031008; AU 2003223892 A8 20031008; EP 1518186 A2 20050330; US 2006075211 A1 20060406; US 2010174868 A1 20100708; US 2015074352 A1 20150312

DOCDB simple family (application)

**DE 0300942 W 20030321**; AU 2003223892 A 20030321; EP 03720231 A 20030321; US 201414540782 A 20141113; US 50855905 A 20050620; US 72909010 A 20100322