

Title (en)

DRAM SUPPORTING DIFFERENT BURST-LENGTH ACCESSES WITHOUT CHANGING THE BURST LENGTH SETTING IN THE MODE REGISTER

Title (de)

DRAM, DER ZUGRIFFE VERSCHIEDENER BURST-LÄNGE UNTERSTÜTZT, OHNE DIE BURST-LÄNGENEINSTELLUNG IM MODUSREGISTER ZU VERÄNDERN

Title (fr)

DRAM SUPPORTANT DES ACCES A LONGUEUR DE RAFALE DIFFERENTE SANS CHANGEMENT DU REGLAGE DE LONGUEUR DE RAFALE DANS LE REGISTRE DE MODE

Publication

**EP 1522021 B1 20070418 (EN)**

Application

**EP 03764378 A 20030709**

Priority

- US 0321286 W 20030709
- US 19382802 A 20020711

Abstract (en)

[origin: WO2004008329A1] A memory device (150) may be implemented to respond to and one or more command encodings that specify different burst lengths than the burst length indicated by the current burst length setting for the memory device. For example, a memory device (150) may include a memory array (152) and a mode register (154) configured to store a value indicating a current burst length. The memory array (152) may be configured to perform a first burst access having a first burst length in response to receiving a first command encoding and to perform a second burst access having a second burst length, which does not equal the current burst length, in response to receiving a second command encoding. A memory controller (100) may be implemented to generate to and one or more command encodings that specify different burst lengths than the burst length indicated by the current burst length setting for a targeted memory device (150).

IPC 8 full level

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CPC (source: EP KR US)

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**WO 2004008329 A1 20040122**; AU 2003258997 A1 20040202; CN 1333353 C 20070822; CN 1669012 A 20050914; DE 60313323 D1 20070531; DE 60313323 T2 20071227; EP 1522021 A1 20050413; EP 1522021 B1 20070418; JP 2005532657 A 20051027; JP 4507186 B2 20100721; KR 101005114 B1 20101230; KR 20050025960 A 20050314; TW 200401191 A 20040116; TW I307464 B 20090311; US 6957308 B1 20051018

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