Title (en)
RECONFIGURABLE SEQUENCER STRUCTURE

Title (de)
REKONFIGURIERBARE SEQUENZERSTRUKTUR

Title (fr)
STRUCTURE DE SEQUENCEUR RECONFIGURABLE

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Abstract (en)
[origin: WO2004038599A1] The invention relates to a cellular element field for data processing, with functional cell means for carrying out algebraic and/or logical functions and memory cell means for receipt, storage and/or output of information. Functional cell/memory cell combinations are thus formed whereby a control connection is run from the functional cell means to the memory cell means.

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See references of WO 2004038599A1

Citation (examination)
YEUNG A K W ET AL: "A reconfigurable data-driven multiprocessor architecture for rapid prototyping of high throughput DSP algorithms", SYSTEM SCIENCES, 1993, PROCEEDING OF THE TWENTY-SIXTH HAWAII INTERNATI ONAL CONFERENCE ON WAILEA, HI, USA 5-8 JAN. 1993, LOS ALAMITOS, CA, USA,IEEE, US, vol. i, 5 January 1993 (1993-01-05), pages 169 - 178, XP010640447, ISBN: 978-0-8186-3230-3, DOI: DOI:10.1109/HICSS.1993.270747

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